

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re Application of

Atty. Docket

NAVEED MAJID ET AL

PHA 23,843

Serial No. 09\440,595

Group Art Unit: 2811

Filed: November 15, 1999

Examiner: N. Parekh

Title: MULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN  
POWER APPLICATIONS

Commissioner for Patents  
Washington, D.C. 20231

#7  
Notice  
of  
Appeal  
F. JONES  
12-21-01

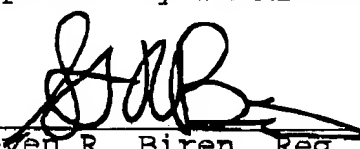
NOTICE OF APPEAL

Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated July 24, 2001 of the Examiner finally rejecting claims 1-7.

- [ X ] Please charge the fee of \$320.00 to Deposit Account No. 14-1270.
- [ ] No additional fee is required, because the fee was paid in a prior appeal.

Respectfully submitted,

By   
Steven R. Biren, Reg. No. 26,531  
Attorney  
(914) 333-9630

12/21/2001 TFLOWER 00000006 141270 09440595

01 FC:119

CERTIFICATE OF MAILING

It is hereby certified that this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to:

COMMISSIONER OF PATENTS AND TRADEMARKS  
Washington, D.C. 20231

On 10/23/01  
(Date of Mailing)

By   
Steven R. Biren, Reg. No. 26,531

S:\BNPL18BISO.RUR.doc

FAX COPY RECEIVED

DEC 14 2001

TECHNOLOGY CENTER 2800

BK OCT 23 2001